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(72) Inventors:
• **Bruno Richard**
38920 Crolles (FR)
• **Denis Chalon**
38000 Grenoble (FR)

(71) Applicant: **Hewlett-Packard Development Company, L.P.**
Houston, TX 77070 (US)

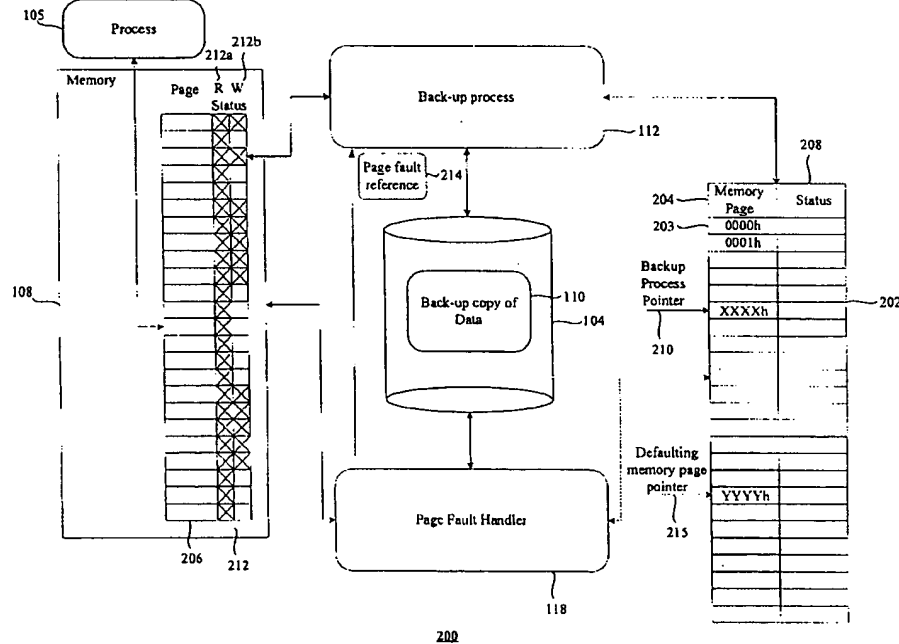
(74) Representative: **Lloyd, Richard Graham**
Hewlett-Packard France
Intellectual Property Section
Etablissement de Grenoble
38053 Grenoble Cedex 09 (FR)

(54) **Method to perform a non-disruptive backup and data processing system therefor**

(57) Embodiments of the present invention provide a data processing method for saving data stored within a portion of a memory associated with a first process executable by a data processing system; the method comprising the steps of: executing, substantially concurrently or substantially in parallel under control of a scheduler, a first process, with a respective execution priority, to perform a data processing task using a cur-

rent portion of the memory and a backup process, having a respective execution priority, to create a back-up copy of data stored within the memory during the performance of that data processing task; the backup process being executable in response to a predeterminable event associated with the first process; and creating a back-up copy of the data in response to the predeterminable event associated with the first process.

Figure 2



Description

Field of the Invention

[0001] The present invention relates to a data processing system and method and, more particularly, to a data processing system and method for performing a back-up operation of data or memory associated with an extensive data processing task.

Background to the Invention

[0002] Many data processing tasks take a considerable amount of time and data processing resources to implement. In many instances, any such processing may take hours, days or, perhaps, even weeks. Often a cluster of data processing engines undertakes such data processing tasks. Each engine works on an atomic data processing task to produce corresponding results. Additionally, or alternatively, a massively parallel computer system can be used to perform such tasks with each processor within such a system being assigned respective atomic data processing tasks.

[0003] It can be appreciated that it would be undesirable if a machine performing such a task, or part of such a task, failed. Therefore, it is known within the art to implement a technique known as *check pointing* in which the data processing task is frozen or temporarily suspended while a complete back-up copy is created of the results, or partial results, thus far. Therefore, if a fault occurs, the previously saved results or partial results, also known as a memory image or picture, are loaded and processing recommences using those results or partial results rather than the whole job or data processing task having to be performed from scratch.

[0004] While the above described prior art technique is able to accommodate hardware or software failures that would, but for the technique, lead to a loss of data processing results and a need to restart the data processing task from scratch, it is undesirable to have to suspend the data processing operation while such a memory dump is performed. This is particularly so in the case where the data processing task is distributed across a number of machines. In such a situation all of the machines, in the worst case, or some of the machines interacting with a machine currently undertaking such check pointing, also have to suspend their operations. Therefore, check pointing undertaken by one machine might entail suspending operations on other machines.

[0005] It is an object of embodiments of the present invention at least to mitigate some of the problems of the prior art.

Summary of Invention

[0006] Accordingly, a first aspect of embodiments of the present invention provides a data processing meth-

od for saving data stored within a portion of a memory associated with a first process executable by a data processing system; the method comprising the steps of: executing, substantially concurrently or substantially in parallel, the first process, with a respective execution priority, to perform a data processing task using a current portion of the memory and a back-up process, having a respective execution priority, to create a back-up copy of data stored within the memory during the performance or pendency of that data processing task; the back-up process being executable in response to a pre-determined event associated with the first process; and creating at least a partial back-up copy of the data in response to the pre-determinable event associated with the first process.

[0007] Advantageously, due to the relative execution priorities of the first process and the back-up process, the back-up process can be arranged to be executing continuously in the background, that is, a scheduler can be arranged, during periods of temporary suspension of the data processing task, or first process, such that the back-up process is executed. Therefore, when the first process encounters a delay such as, for example, a communication delay or a device delay when, for example, writing to or reading from an HDD or outputting data for transmission over a network, the first process will be temporarily suspended and the processing capacity or resources of the system can be made available to the back-up process, which continues its back-up operations until the first process is re-started. This is in contrast to the prior art in which the whole of the back-up process is completed before execution of the main processing task is allowed to resume.

[0008] Preferably, embodiments of the present invention provide a method in which the step of creating the back-up copy of the data stored within the memory comprises the step of progressively traversing the memory to create a back-up copy of a respective portion of the memory; the respective portion, preferably, being different to the current portion of the memory used by the first process.

[0009] Preferred embodiments provide a method further comprising the step of detecting an event associated with a memory access to the current portion of the memory used by the first process and creating a back-up copy of that current portion of the memory.

[0010] A further aspect of embodiments of the present invention provides a back-up method for a data processing system comprising the steps of progressively or incrementally creating back-up copies of respective portions of data or memory associated with a first process and, in response to an interrupt or an event, temporarily suspending the progressive or incremental back-up process and performing an asynchronous back-up of an identifiable portion of memory associated with the interrupt or event.

[0011] A further aspect of embodiments of the present invention provides an interrupt driven back-up process

comprising the steps of performing a progressive or incremental back-up of a portion of memory and responding to an interrupt to back-up a portion of memory other than the respective portions of memory associated with a memory operation of a first process.

[0012] Preferably, embodiments provide a method comprising the step of changing a read-write status of a current portion of memory to a read-only status; and in which the step of detecting the event comprises a step of detecting a write attempt to the read-only current portion of the memory.

[0013] Preferred embodiments provide a method further comprising the step of varying the relative execution priorities of the first process and the back-up process such as, for example, increasing the execution priority of the back-up process in response to detection of a period of micro-inactivity or in response to detection of a page fault to allow the back-up process to resume or to be performed.

[0014] Embodiments, preferably, provide an interrupt driven back-up process comprising the steps of initiating and performing a first back-up process; interrupting the first back-up process to perform a second asynchronous or non-sequential back-up process; and resuming the first back-up process having performed or completed the second back-up process.

[0015] Preferred embodiments are provided in which the step of varying comprises the step of establishing an execution priority for the back-up process that is greater than the execution priority of the first process. Preferably, the step of varying comprises the step of establishing relative execution priorities for the first process and the back-up process such that the back-up process execution priority is greater than the execution priority of the first process.

[0016] Preferred embodiments provide a method in which the step of varying is responsive to detection of the predeterminable event associated with the first process or the memory access to the current portion of the memory used by the first process.

[0017] Preferably, embodiments provide a method further comprising the step of installing a page fault handler associated with the memory; the page fault handler being responsive to detection of an event to perform the step of saving a copy of the page associated with the event and to write to the page associated with the event following said saving.

[0018] A further aspect of embodiments of the present invention provides a data processing method for creating a back-up copy of data stored within a memory associated with a first process; the method comprising the steps of progressively or incrementally creating a back-up copy of serial or sequential portions of the memory in response to an event associated with the first process; and interrupting the progressive or incremental back-up process to perform an asynchronous, that is, non serial or sequential, back-up of a further portion of the memory; the further portion of the memory being different to

the serial or sequential portions of the memory.

[0019] A still further aspect of embodiments of the present invention provides a system comprising means to implement a method as claimed or described herein.

[0020] Further embodiments provide a computer program or computer program element comprising computer executable code to implement a method or system as described or claimed herein. Preferably, embodiments provide a computer program product comprising computer readable storage storing such a computer program or computer program element.

Brief Description of the Drawings

[0021] Embodiments of the present invention will now be described, by way of example only, with reference to the accompanying drawings in which:

figure 1 shows, schematically, a data processing system according to an embodiment;

figure 2 depicts back-up operations associated with the data processing system shown in figure 1;

figure 3 shows, schematically, a basic back-up process according to an embodiment; and

figure 4 shows an interrupt-driven back-up process according to a second embodiment.

Detailed Description of the Preferred Embodiments

[0022] Figure 1 shows, schematically, a data processing system 100 according to an embodiment. The data processing system 100 comprises a computer system 102 and, preferably, non-volatile storage 104 or at least to such storage. Preferably, the non-volatile storage takes the form of an HDD. The computer system 102 comprises a first process 105 for performing a data processing task. The first process 105 generates data 106 that are stored within a memory 108 of the data processing system 100. The non-volatile storage 104 stores a back-up copy 110 of the data 106. A back-up process 112 creates the back-up 110 copy of the data 106.

[0023] The computer system 102 also comprises an operating system or OS kernel 114 that has, amongst other things, an execution scheduler 116 and a page fault handler 118. The execution scheduler 116 manages the use of the resources of the computer system 102 to allow the first process 105 and the back-up process 112 to execute ostensibly in parallel. In effect, the scheduler 116 implements or realises a multi-tasking or multi-threaded data processing environment. The scheduler 116 is arranged to attribute a relatively high execution priority to the first process 105 and a relatively low execution priority to the back-up process 112. The scheduler 116 assigns the relative execution priorities to the

first process 105 and the back-up process 112 such that the first process 105 is given access to the resources (not shown) of the computer system while that process 105 needs those resources. On occasions when the first process 105 does not need the resources of the computer system 102, that is, on occasions when the processing performed by the first process 105 is temporarily suspended, the resources of the computer system 102 are made available to the back-up process 112. The first process 105 is said to be pending, that is, still scheduled for execution by the scheduler 116 but in a waiting state from the perspective of the scheduler. In preferred embodiments, the waiting state of the first process 105 represents an embodiment of a predetermined or predeterminable event associated with the first process. In preferred embodiments, the suspended, but still scheduled, first process, remains in this waiting state until a predetermined event occurs. As mentioned above, such a predetermined event might relate to a device response. This is in contrast to the prior art in which the first process 105 would be de-scheduled during back-up or check pointing until the whole of the back-up process has been completed.

[0024] It will be appreciated that the computer system 102 also comprises a processor, as is conventional, for executing the various software elements described above or in this specification.

[0025] The back-up process 112 is arranged to create, incrementally or progressively, the back-up copy 110 of the data 106 during periods for which it is granted access to the computing resources of the computer system 102 by the scheduler 116. In preferred embodiments, the back-up software 112 saves the data 106 by saving respective pages (not shown) of the memory 108 associated with, or used by, the first process 105. The back-up process 112 saves as many of those pages as possible using the resources and time granted to it by the scheduler 116. The back-up process 112 is arranged to perform the back-up process serially or sequentially by saving the data of the first process 105 in the order in which the data was produced. The information saved by the back-up process 112 is, in preferred embodiments, an exact copy of the memory image, or at least a portion, such as, for example, a page, of the memory image.

[0026] The periods for which the back-up process 112 is granted access to the computing resources of the computer system 102 are those for which the first process 105 is temporarily suspended. These periods, known as periods of micro-inactivity, typically occur when the first process 105 has performed an operation for which a response is required. For example, the first process may have instigated a read or write operation to a device and that read or write operation will take a specific period of time. If the first process 105 is unable to continue until confirmation of that operation has been received or until data associated with that operation has been received, the processing performed by the first

process 105 will be temporarily suspended. It will be appreciated that such periods of inactivity do not represent a suspension or freezing of the first process 105 in the sense of the above described prior art check pointing technique since the first process 105 is still executing or pending from the perspective of the user or the scheduler and that it is merely in a waiting state.

[0027] The page fault handler 118, according to embodiments of the present invention, responds to page faults associated with the first process 105 by storing, on the HDD 104, a copy (not shown) of the page of memory identified by the page fault. Preferably, the copy of the defaulting page is stored as part of the copy 110 of the data 106. It can be appreciated that the page fault handler 118 represents an embodiment of an interrupt-driven back-up process that supplements or augments the serial or sequential back-up process performed by the back-up process 112.

[0028] Figure 2 shows the processing 200 performed by the elements of the data processing system 100 shown in figure 1. The back-up process 112 comprises, or has access to, a table 202 containing identifiers 204 of the pages 206 of the memory 108 associated with the first process 105 together with respective status indicia 208. The status indicia 208 provide an indication to the back-up process 112 of whether or not corresponding pages of memory 206, identified by the respective memory page identifiers 204, have been backed-up.

[0029] The back-up process 112 uses a back-up process pointer 210 to step through or traverse the table 204 progressively or incrementally and to create, in a similar progressive or incremental manner, back-up copies of respective pages of memory 206, starting with the first entry 203 of the table 202. It will be appreciated that the back-up copy 110 of the data 106 is created by the back-up process 112 progressively and, preferably, in discrete units, which are, in preferred embodiments, pages 206 of the memory 108.

[0030] Preferably, the back-up process 112 modifies the read/write status flags 212 associated with the pages 206 of memory 108 such that they are read only. The first flag 212a represents the current writeable status of the memory prior to the change made by the back-up process. The second flag 212b represents the original writeable status of the page. Therefore, for example, if a page was intended to be read-only, the first and second flags would be set to RO and the first process would not be allowed to write to those protected pages. Another example is if a page is intended to be read-write, the second flag is set to RW and the first flag is switched from RW to RO. This arrangement will trigger a page fault if a write by the first process 105 is performed. In any event, once a page has been written to memory by the back-up process, the first flag (current writeable status) is set back to the second flag's value (original writeable status).

[0031] The page fault handler 118 is arranged to respond to a write operation instigated by the first process

105 to one of the read-only memory pages 206 by creating, or causing the back-up process 112 to create, a back-up copy of the page associated with, or identified by, the page fault exception. The page fault handler 118 represents an embodiment of detector for detecting and responding to page faults. Preferably, the page fault handler 118 passes a reference 214 or identifier of the defaulting memory page to the back-up process 112 to make a non-sequential or asynchronous back-up of the defaulting page of memory. It can be appreciated that the back-up process 112 is shown as processing a page identified by a notional hexadecimal address, XXXXh, and the page fault handler 118 is shown as processing a different page identified by a different notional hexadecimal address, YYYYh. The reference 214 is used by a defaulting memory page pointer 215 to access the defaulting page and to record in the table 202 appropriate status information 208 showing the defaulting page to have been backed-up.

[0032] In preferred embodiments, the page fault handler 118 replaces the conventional OS page fault handler. The replacement is preferably at least for the duration of the back-up process. Upon completion of the back-up process, the original page fault handler can be reinstalled or reactivated. The page fault handler preferably re-installs the original or a conventional page fault handler upon receiving an indication from the back-up process 112 that the back-up operation has been completed. Alternatively, the back-up process 112 itself might be responsible for commissioning and decommissioning the appropriate page fault handlers. The page fault handler 118 or the back-up process 112, coupled with the processor might represent embodiments of an installer. Therefore, the first process might be interrupted by the back-up process during a period other than a period of micro-inactivity when the OS or backup process installs the new handler 118. Furthermore, it will be appreciated that the handler 118 can be arranged to respond to page faults generated by or associated with only the first process 105 in preferred embodiments. Still further, page faults associated with processes other than the first process are handled conventionally.

[0033] Figure 3 shows a flowchart 300 of the processing steps performed by the data processing system 100 according to a basic embodiment. The back-up table 202, containing the list of the identifiers 204 of memory pages 206 associated with the first process 105, together with the status information 208, showing whether or not the corresponding pages 206 have been backed-up, is created at step 302. The back-up process pointer 210 is initialised to point to the first memory page identifier 203 stored within the table 202 at step 304. A test is performed, at step 306, to determine whether or not all of the pages 206 of memory 108 associated with the first process 105 have been backed-up. If all pages 206 of memory 108 associated with the first process 105 have been backed-up, the check pointing or back-up operation is deemed to be complete and processing termi-

nates at step 308. If the determination at step 306 is negative, the back-up process 112, at step 310, creates a back-up copy of the page 206 of memory 108 identified by the memory page identifier 204 pointed to by the back-up process pointer 210. The back-up process pointer 210 is incremented at step 312 so that it points to the next memory page identifier in the list of memory page identifiers. Processing then returns to step 306 where the test to determine whether or not there are further memory pages 206 to back-up is performed again. Steps 306, 310 and 312 are performed until all pages of memory 206 have been backed-up, that is, until all memory pages associated with the first process 105 have been backed-up.

[0034] It will be appreciated that the back-up process 112, due to its execution priority relative to that of the first process 105, will perform the back-up of the memory pages 206 in a sequential or progressive manner that might be sporadic according to the demands placed upon the resources of the computer system 102 by the first process 105, which, in turn, influences the availability of those resources to the back-up process 112.

[0035] Figure 4 depicts a pair of flowcharts 400 of a back-up process according to a second embodiment.

The back-up table 202 is created at step 402. Also performed at step 402 is a process for making all, or at least some, of the memory pages 206 associated with the first process 105 read-only. This is achieved by changing the read/write status flags 212 accordingly. An optional step of replacing a current page fault handler, different to the page fault handler 118, with the page fault handler 118 of the embodiments of the present invention is performed at step 404. The back-up process current page pointer 210 is initialised, at step 406, to point to the first entry 203 in the table 202. A test is performed, at step 408, to determine whether the table 202 contains identifiers 204 for memory pages 206 that have not been backed-up. If it is determined, at step 408, that there are no more pages to be backed-up, processing proceeds to the optional step of 410 where the original page fault handler is restored thereby replacing the page fault handler 118 according to an embodiment of the present invention.

[0036] If the test performed at step 408 shows that there are further pages 206 of memory 108 to be backed-up, the back-up process 112 creates a copy of the memory page 206 identified by the back-up process current pointer 210. The back-up process current page pointer 210 is incremented at step 414 to point to the next memory page identifier 204 contained within the table 202. Processing then returns to step 408 where the test is performed again.

[0037] Since the memory pages 206 associated with the first process 105 were made read only at step 402, any attempt made by the first process 105 to write to those pages will generate a page fault interrupt that is handled by the newly installed page fault handler 118. The processing shown by the left-hand flowchart of fig-

ure 4 continues until all of the memory pages 206 have been backed-up, the processing resources of the computer system are returned to the first process 105 or the page fault handler 118 interrupts that back-up process to perform an asynchronous back-up process; the steps of which are shown in the right-hand flowchart of figure 4.

[0038] Still referring to figure 4, the page causing the page fault is identified to the back-up process 112 at step 416. A determination is made at step 418 as to whether or not the page associated with the page fault has already been backed-up using the appropriate status information 208 stored within the back-up table 202. If it is determined that a back-up of the page associated with the page fault has already been created, the write operation to that page is allowed to proceed at step 422. If it is determined from the status information 208 that the memory page associated with the page fault has not already been backed-up, a back-up copy of that page is created at step 420. Once a back-up copy of the memory page 206 causing, or associated, with the page fault has been created, the write operation is allowed to proceed at step 422.

[0039] It will be appreciated that the back-up process is arranged to produce, progressively or incrementally, a complete snapshot or memory image of the memory used by the first process as it was at the point in time when the back-up process was commenced. This is desirable to preserve the atomicity of the back-up process or snapshot. Therefore, the back-up process 112 saves a page of memory that is about to be modified before it is actually modified to preserve the memory image of that page as it was when the back-up process commenced. Having completed the asynchronous back-up process shown in the right-hand flowchart of figure 4, processing returns to the appropriate point within the serial or sequential back-up process shown in the left-hand flowchart of figure 4 or, if the page fault occurred while the first process was running, processing returns to the first process 105. It will be appreciated that the back-up process is able to keep track of the pages that have been backed-up via the handler 118 to ensure that the atomicity of the back-up is preserved, that is, to ensure that a page previously backed-up by the handler 118 is not also backed-up by the back-up process 112.

[0040] Although the above embodiments have been described with reference to the page fault handler 118 providing an identifier of the page associated with the page fault to the back-up process 112 so that the latter can perform a back-up of that page, embodiments are not limited to such an arrangement. Embodiments can be realised in which the page fault handler itself performs the necessary processing to create the back-up copy of the defaulting page and to amend the associated status information 208 accordingly.

[0041] It will be appreciated that preferred embodiments realise the above-described elements, that is, the back-up process, the page fault handler and the first

process in software. Therefore, embodiments of the present invention are provided in which the software is stored on or in a computer readable medium such as, for example, magnetic or optical discs or ROM, PROMs or the like.

[0042] Although the above embodiments have been described with reference to a first process being performed by a single computer system, embodiments are not limited to such an arrangement. Embodiments can be realised in which a cluster of such computer systems is used to perform the first process or respective processes of an overall processing task.

[0043] The reader's attention is directed to all papers and documents which are filed concurrently with or previous to this specification in connection with this application and which are open to public inspection with this specification, and the contents of all such papers and documents are incorporated herein by reference.

[0044] All of the features disclosed in this specification (including any accompanying claims, abstract and drawings) and/or all of the steps of any method or process so disclosed, may be combined in any combination, except combinations where at least some of such features and/or steps are mutually exclusive.

[0045] Each feature disclosed in this specification (including any accompanying claims, abstract and drawings) may be replaced by alternative features serving the same, equivalent or similar purpose, unless expressly stated otherwise. Thus, unless expressly stated otherwise, each feature disclosed is one example only of a generic series of equivalent or similar features.

[0046] The invention is not restricted to the details of any foregoing embodiments. The invention extends to any novel one, or any novel combination, of the features disclosed in this specification (including any accompanying claims, abstract and drawings), or to any novel one, or any novel combination, of the steps of any method or process so disclosed.

Claims

1. A data processing method for saving data stored within a portion of a memory associated with a first process executable by a data processing system; the method comprising the steps of: executing, substantially concurrently or substantially in parallel under control of a scheduler, a first process, with a respective execution priority, to perform a data processing task using a current portion of the memory and a backup process, having a respective execution priority, to create a back-up copy of data stored within the memory during the performance of that data processing task; the backup process being executable in response to a predeterminable event associated with the first process; and creating a back-up copy of the data in response to the predeterminable event associated with the first process.

- ess.
2. A method as claimed in claim 1 in which the step of creating the back-up copy of the data stored within the memory comprises the step of progressively traversing the memory to create a back-up copy of a respective portion of the memory; the respective portion being different to the current portion of the memory used by the first process. 5
 3. A method as claimed in any preceding claim, further comprising the step of detecting an event associated with a memory access to the current portion of the memory used by the first process and creating the back-up copy of that current portion of the memory. 10
 4. A method as claimed in claim 3 further comprising the step of changing a read/write status of at least a portion of memory to read-only; and in which the step of detecting the event comprises the step of detecting a write attempt to the read-only current portion of memory. 20
 5. A method as claimed in any preceding claim further comprising the step of installing a page fault handler associated with the memory; the page fault handler being responsive to detection of an event associated with a page to save a copy that page and then to allow a write operation to that page. 25
 6. A data processing method for creating a back-up copy of data stored within a memory associated with an on-going or currently executing first process; the method comprising the step of progressively or incrementally creating a back-up copy of at least a number of portions of the memory in response to an event associated with the first process. 30
 7. A data processing system for saving data stored within a portion of a memory associated with a first process; the system comprising a processor for executing, substantially concurrently or substantially in parallel under control of a scheduler, the first process, with a respective execution priority, to perform a data processing task using a current portion of the memory and a backup process, having a respective execution priority, to create a back-up copy of data stored within the memory during the performance of that data processing task; the backup process being executable in response to a predetermined event associated with the first process; and creating a back-up copy of the data in response to the predetermined event associated with the first process. 35
 8. A system as claimed in claim 7 in which the back-up process for creating the back-up copy of the data stored within the memory comprises means for progressively traversing the memory to create a back-up copy of a respective portion of the memory; the respective portion being different to the current portion of the memory used by the first process. 40
 9. A system as claimed in either of claims 7 and 8 further comprising a detector for detecting an event associated with a memory access to the current portion of the memory used by the first process and creating the back-up copy of that current portion of the memory. 45
 10. A system as claimed in claim 9 further comprising means to change a read/write status of the current portion of memory to read-only; and in which the detector for detecting the event comprises means to detect a write attempt to the read-only current portion of memory. 50
 11. A system as claimed in any of claims 7 to 10 further comprising an installer to install a page fault handler associated with the memory; the page fault handler being responsive to detection of an event associated with a page to save a copy that page and then to allow a write operation to that page. 55
 12. A data processing system for creating a back-up copy of data stored within a memory associated with an on-going or currently executing first process; the system comprising means to progressively or incrementally create a back-up copy of at least a number of portions of the memory in response to an event associated with the first process.
 13. A data processing method for saving data stored within a portion of a memory associated with a first process executable by a data processing system; the method comprising the steps of: executing, using multitasking under control of a scheduler, a first process, having a respective execution priority, for performing a data processing task using a current portion of the memory and a backup process, having a respective execution priority, for creating a back-up copy of a data stored within the memory; the execution priority of the first process being greater than the execution priority of the back-up process.
 14. A data processing system for saving data stored within a portion of a memory associated with a first process executable by a data processing system; the system comprising the steps of: a processor for executing, substantially concurrently or substantially in parallel under control of a scheduler, a first process, with a respective execution priority, to perform a data processing task using a current portion

of the memory and a backup process, having a respective execution priority, to create a back-up copy of data stored within the memory during the performance of that data processing task; the backup process being executable in response to a predetermined event associated with the first process; and the back-up process comprising means to create a back-up copy of the data in response to the predetermined event associated with the first process.

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15. A computer program comprising computer executable code to implement a method or system as claimed in any preceding claim.

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16. A computer program product comprising computer readable storage storing a computer program as claimed in claim 21.

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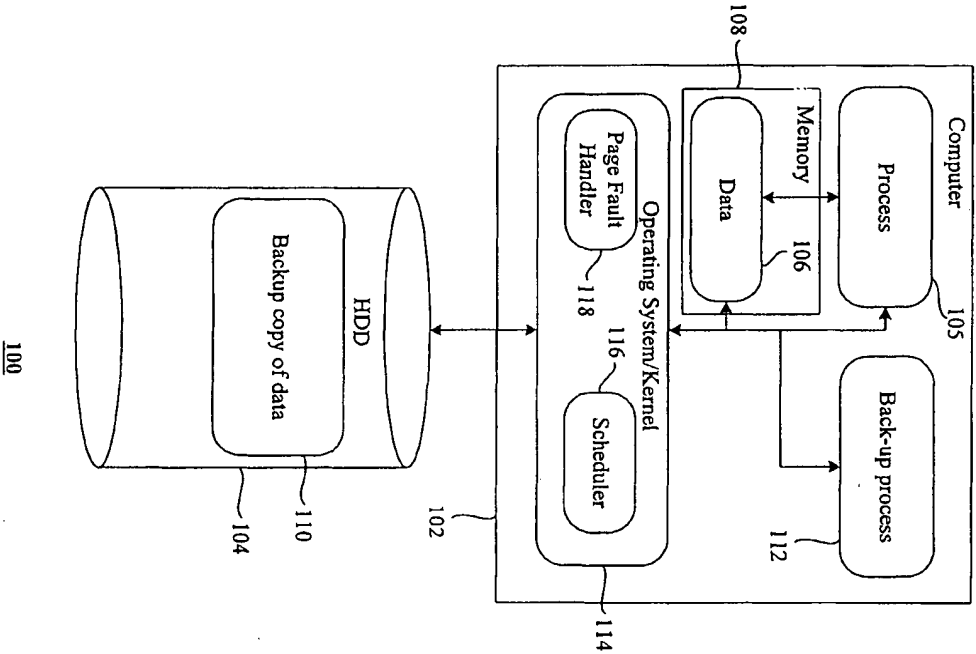
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Figure 1



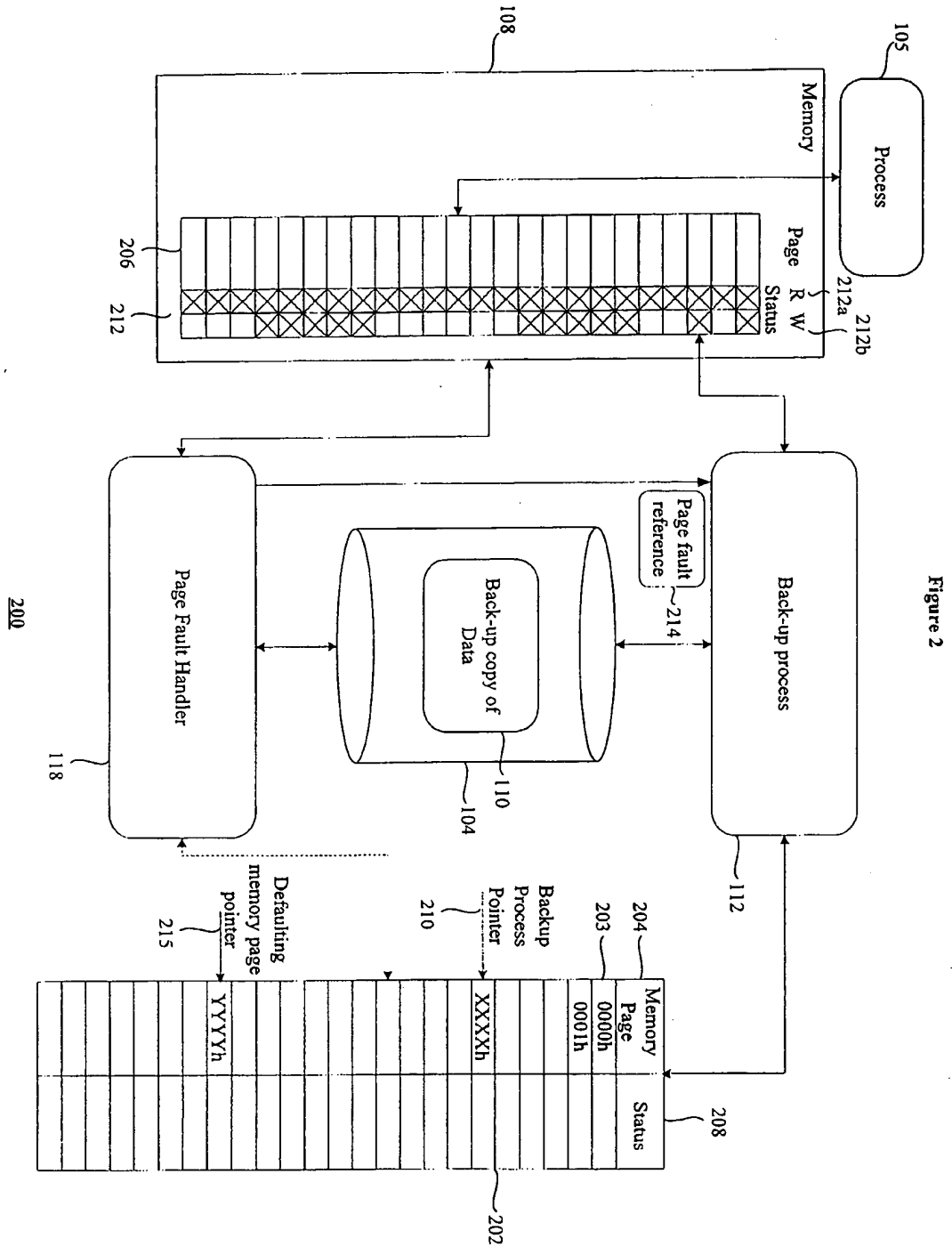
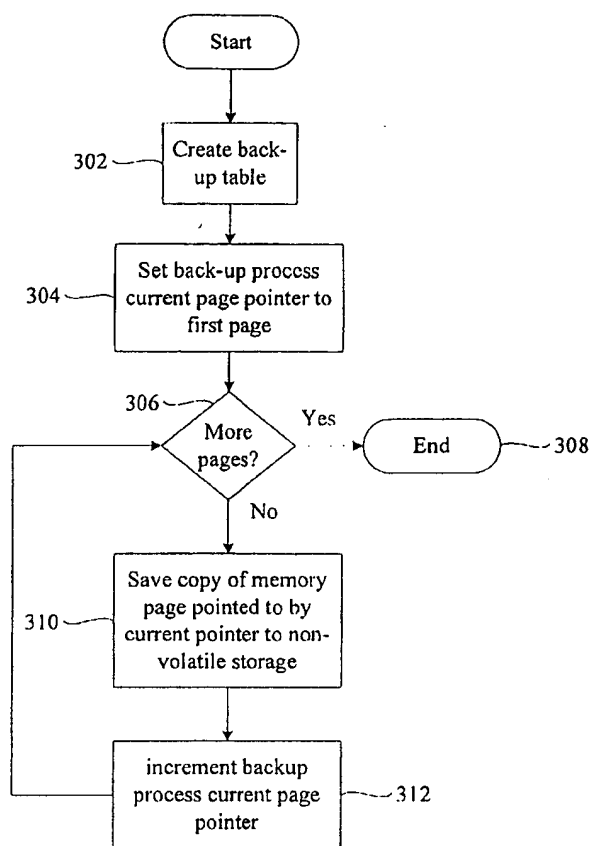
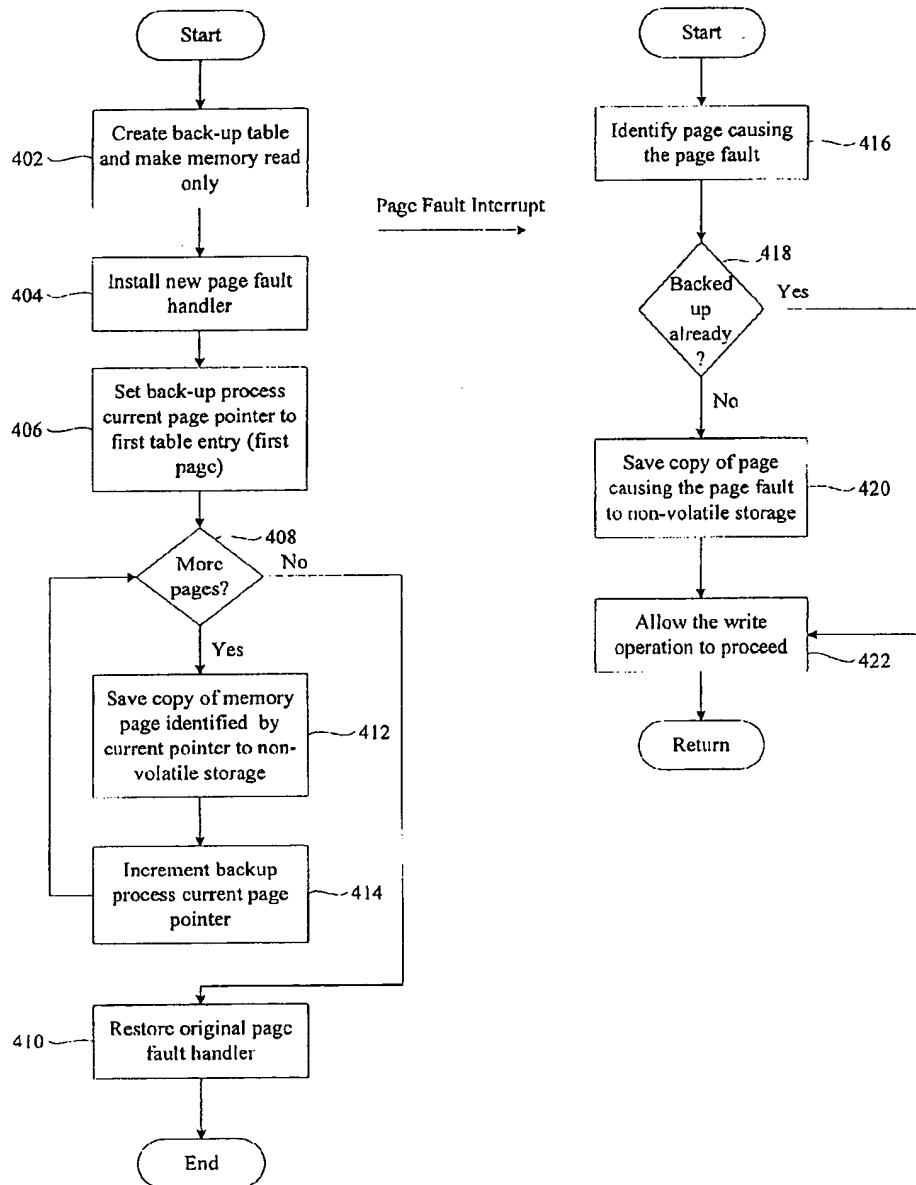


Figure 3



300

Figure 4



400



European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
EP 03 29 1313

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
X	WO 97 24668 A (IPL SYSTEMS INC) 10 July 1997 (1997-07-10)	1-4, 6-10,12, 14-16 13	G06F11/14
Y	* abstract *	5,11	
A	* page 3, line 20 - line 23 * * page 5, line 15 - line 21 * * page 6, line 18 - page 7, line 26 * * page 8, line 26 - page 9, line 16 * * page 10, line 1 - line 15 * * page 11, line 12 - line 15 * * page 16, line 21 - page 17, line 6 * * figures 2,3 *		
Y	--- EP 0 869 437 A (OGAWA ATSURO) 7 October 1998 (1998-10-07) * abstract *	13	
A	* column 8, line 22 - line 41 *	1-12, 14-16	
A	--- "TIMELY POLICY-BASED EVENT-DRIVEN BACKUPS OF CRITICAL FILES" IBM TECHNICAL DISCLOSURE BULLETIN, IBM CORP. NEW YORK, US, vol. 39, no. 4, 1 April 1996 (1996-04-01), pages 127-129, XP000587447 ISSN: 0018-8689 * the whole document *	1,3,6,7, 9,12-16	TECHNICAL FIELDS SEARCHED (Int.Cl.7) G06F
The present search report has been drawn up for all claims			
Place of search MUNICH		Date of completion of the search 11 November 2003	Examiner Leuridan, K
CATEGORY OF CITED DOCUMENTS X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same category A: technological background O: non-written disclosure P: intermediate document		T: theory or principle underlying the invention E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons &: member of the same patent family, corresponding document	

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ON EUROPEAN PATENT APPLICATION NO.**

EP 03 29 1313

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
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11-11-2003

Patent document cited in search report		Publication date	Patent family member(s)		Publication date
WO 9724668	A	10-07-1997	WO	9724668 A1	10-07-1997
EP 0869437	A	07-10-1998	JP	10333948 A	18-12-1998
			EP	0869437 A2	07-10-1998
			US	6108671 A	22-08-2000

EPO FORM P0459

For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

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